		Application/Control No.	Applicant(s)/Patent Under Reexamination
Notice of References Cited		09/918,734	JAIN ET AL.
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Notice of References Cited	Application/Control No. 09/918,734	Applicant(s)/Patent Under Reexamination JAIN ET AL.	
	Examiner Russell L. Guill	Art Unit 2123	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,289,488	09-2001	Dave et al.	716/1
*	B	US-6,305,006 B1	10-2001	Markov, Lev A.	716/18
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, Wayne and Xie, Yuan; "Allocation and scheduling of conditional task graph in hardware/software co-synthesis", March 2001, Proceedings of the conference on Design, Automation and Test in Europe, pages 620-625
	V	Dick, Robert P. and Jha, Niraj K.; "MOGAC: A multiobjective genetic algorithm for the co-synthesis of hardware-software embedded systems", November 1997, Digest of Technical Papers, 1997 IEEE/ACM International Conference on Computer-Aided Design
	W	Kalavade, Asawaree and Subrahmanyam, P. A.; "Hardware/software partitioning for multifunction systems", September 1998, IEEE Transactions on Computer-Aided Design of Integrated Circuit and Systems, Vol. 17, No. 9
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.